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#52131



Patent 30
PD-200337
Customer No. 020991

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Cocchi, Ronald P., :
et al. :

Serial No.: 10/085,920 :
Filed: February 28, 2002 :

For :

DEDICATED NONVOLATILE
MEMORY :

Group Art Unit: 2131 :
Examiner: to be determined :

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Georgann S. Grunebach

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Date: August 20, 2004

INFORMATION DISCLOSURE STATEMENT RECEIVED

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Technology Center 2100

Dear Sir:

The references cited on the enclosed forms PTO/SB/08A and PTO/SB/08B may be material to the examination of the above-identified patent application and are, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR 1.56. In accordance with the recent Notice of the USPTO regarding the submission of Information Disclosure Statements without copies of the U.S. patents and published applications in patent applications filed after June 30, 2003, Applicant submits herewith copies of foreign patent documents and non-patent literature only, if applicable. It is respectfully requested that the references be considered and made of record in the application identified above.

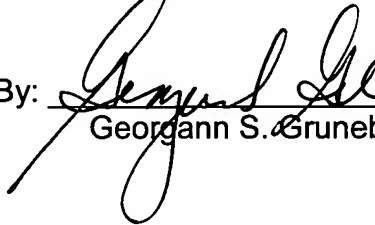
This Information Disclosure Statement is not to be construed as a representation that a search has recently been made, that additional information material to the examination of this application does not exist, or that this citation constitutes prior art.

It is believed that no fee is due with this submission. If a fee is due, the Commissioner is hereby authorized to charge any additional fees which may be required, or credit any

overpayment, to Deposit Account No. 50-0383 of Hughes Electronics Corporation, El Segundo, California.

Respectfully submitted,

By:


Georgann S. Grunebach, Registration No. 33,179

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Application Number	10/085,920
Filing Date	February 28, 2002
First Named Inventor	Cocchi, Ronald P.
Art Unit	2131
Examiner Name	to be determined
Attorney Docket Number	PD-200337

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 6,026,020			
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		EP 0 851 358				
		EP 0 971 361				
		EP 0 984 403				
		EP 1 050 821				
		EP 1 085 516				
		EP 1 176 826				

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Application Number	10/085,920
Filing Date	February 28, 2002
First Named Inventor	Cocchi, Ronald P.
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				
		EP 1 074 906				
		WO 00 77717				
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

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of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		ARAKI, SHIGEO, "The Memory Stick", IEEE Micro, ISSN: 0272-1732, vol. 20, no. 4, July 2000 (2000-07)-August 2000 (2000-08), pages 40-46, XP002257044	
		"Functional Model of a Conditional Access System", EBU Review-Technical, European Broadcasting Union. Brussels, Belgium, no. 266, 21 December 1995 (1995-12-21), pp. 64-77, XP000559450.	
		KHOURI, OSAMA, et al., "Low Output Resistance Charge Pump for Flash Memory Programming", IEEE International Workshop on Memory Technology, Design and Testing (MTDT '01), San Jose, USA.	

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